

# ***Examination Timetable January 2026***

<i><b>Exam Title</b></i>	<i><b>Stage</b></i>	<i><b>Start Date</b></i>	<i><b>Start Time</b></i>	<i><b>Finish Time</b></i>	<i><b>Size</b></i>	<i><b>Venue</b></i>
<i><b>DK_EELES_7Y1 - Electronic &amp; Elec Sys Level 7</b></i>						
ELECE8006_Analogue Electronics	Y1	06/01/2026	12:15:00	14:15:00	30	Faulkner Hall (MPC)
MATHE7011_Mathematics 1	Y1	08/01/2026	09:15:00	11:15:00	24	Faulkner Hall (MPC)
ELECE7019_Electrical Circuits	Y1	12/01/2026	12:15:00	14:15:00	23	P1313 (PJ Carrols Building)

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<i><b>DK_EELES_7Y2 - Electronic &amp; Elec Sys Level 7</b></i>						
ELTRE7018_Sensors	Y2	05/01/2026	09:15:00	11:15:00	17	P1313 (PJ Carrols Building)
ELECE8008_Signal Conditioning	Y2	08/01/2026	09:15:00	11:15:00	17	Faulkner Hall (MPC)
ELECE8010_Electrical Engineering	Y2	12/01/2026	12:15:00	14:15:00	47	M111/M113 - Muirhevna Building
MATHE8020_Mathematics - Circuit Analysis	Y2	15/01/2026	12:15:00	14:15:00	17	Faulkner Hall (MPC)

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<i><b>DK_EELES_7Y3 - Electronic &amp; Elec Sys Level 7</b></i>						
ELECE8011_Electronic Product Development	Y3	05/01/2026	12:15:00	14:15:00	30	Faulkner Hall (MPC)
ELECE7013_Electrical Machines	Y3	07/01/2026	12:15:00	14:15:00	32	Faulkner Hall (MPC)
ENGRE7018_Applied Control Engineering	Y3	09/01/2026	12:15:00	14:15:00	32	P1313 (PJ Carrols Building)
ELECE8010_Electrical Engineering	Y3	12/01/2026	12:15:00	14:15:00	47	M111/M113 - Muirhevna Building
ELTRE8021_Embedded Systems Design	Y3	16/01/2026	09:15:00	11:15:00	44	Conference Centre (Back of Faulkner Hall)

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