

Examination Timetable Autumn 2025

<i>Exam Title</i>	<i>Stage</i>	<i>Start Date</i>	<i>Start Time</i>	<i>Finish Time</i>	<i>Size</i>	<i>Venue</i>
<i>DK_EELEG_8Y1 - Electrical and Elec Eng Level8</i>						
MATHE8007_Engineering Mathematics 1	Y1	18/08/2025	09:15:00	11:15:00	6	Faulkner Hall (MPC)
ELECE8002_Dynamics	Y1	21/08/2025	09:15:00	11:15:00	4	Faulkner Hall (MPC)
MATHE8008_Engineering Mathematics 2	Y1	27/08/2025	09:15:00	11:15:00	7	Faulkner Hall (MPC)
ELTRE8002_Statics	Y1	28/08/2025	12:15:00	14:15:00	4	M111/M113 - Muirhevna Building

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<i>DK_EELEG_8Y2 - Electrical and Elec Eng Level8</i>						
ELTRE7018_Sensors	Y2	28/08/2025	09:15:00	11:15:00	5	Faulkner Hall (MPC)

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<i>DK_EELEG_8Y3 - Electrical and Elec Eng Level8</i>						
ELECE7012_Electrical Engineering	Y3	21/08/2025	09:15:00	12:15:00	5	Conference Centre (Back of Faulkner Hall)
ELECE7011_Electronic Systems Design	Y3	26/08/2025	12:15:00	14:15:00	2	M111/M113 - Muirhevna Building
ENGRE7047_Engineering Design 2	Y3	27/08/2025	12:15:00	14:15:00	2	Faulkner Hall (MPC)
ENGRE7018_Applied Control Engineering	Y3	29/08/2025	12:15:00	14:15:00	5	Faulkner Hall (MPC)

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<i>DK_EELEG_8Y4 - Electrical and Elec Eng Level8</i>						
ELTRE8017_Control Systems & Automation	Y4	18/08/2025	09:15:00	12:15:00	2	Conference Centre (Back of Faulkner Hall)
ELTRE8018_Embedded Systems Design 2	Y4	19/08/2025	12:15:00	14:15:00	2	Faulkner Hall (MPC)
ELECE8004_Power Grid Systems	Y4	20/08/2025	09:15:00	11:15:00	2	Faulkner Hall (MPC)
COMME8002_Data Communications	Y4	21/08/2025	12:15:00	14:15:00	3	Faulkner Hall (MPC)
ELTRE8016_Embedded Systems Design 1	Y4	22/08/2025	12:15:00	14:15:00	2	Faulkner Hall (MPC)
ELECE8005_Power Electronics Design	Y4	26/08/2025	09:15:00	11:15:00	2	Faulkner Hall (MPC)
ENGRE8030_Power System Engineering	Y4	27/08/2025	12:15:00	14:15:00	3	Faulkner Hall (MPC)
COMME8003_Communications Systems	Y4	28/08/2025	09:15:00	11:15:00	2	Faulkner Hall (MPC)
ENGRE8037_Innovation Management	Y4	29/08/2025	09:15:00	11:15:00	5	Faulkner Hall (MPC)

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